

(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property  
Organization  
International Bureau



(43) International Publication Date  
24 June 2004 (24.06.2004)

PCT

(10) International Publication Number  
**WO 2004/054094 A1**

(51) International Patent Classification<sup>7</sup>: **H03F 1/30**

(21) International Application Number:  
PCT/IB2003/005395

(22) International Filing Date:  
25 November 2003 (25.11.2003)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:  
60/431,842 9 December 2002 (09.12.2002) US

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(81) Designated States (*national*): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.

(84) Designated States (*regional*): ARIPO patent (BW, GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PT, RO, SE, SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

**Declaration under Rule 4.17:**

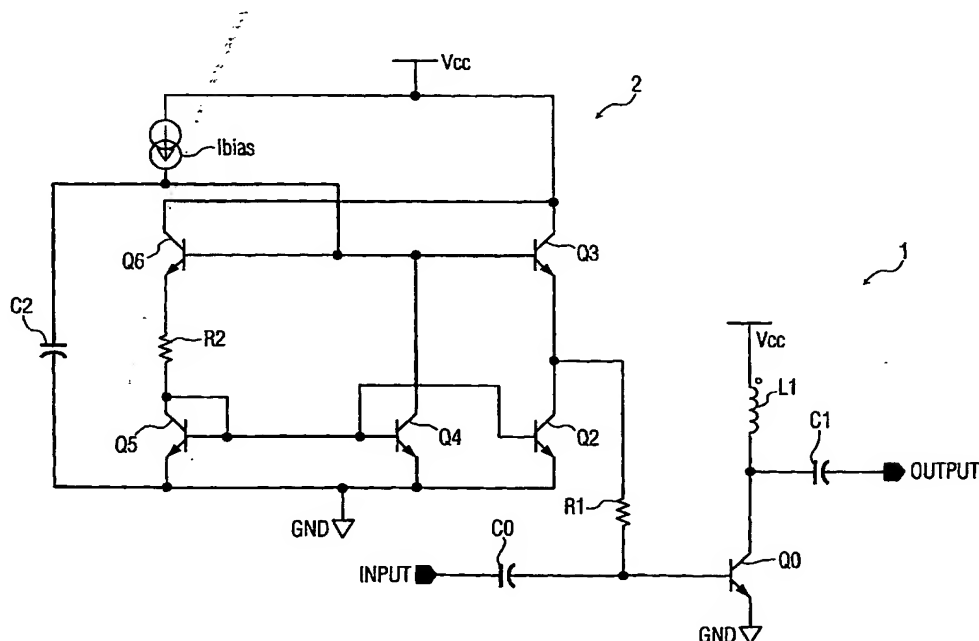
— as to applicant's entitlement to apply for and be granted a patent (Rule 4.17(ii)) for all designations

**Published:**

— with international search report

[Continued on next page]

(54) Title: **AMPLIFIER CIRCUIT HAVING AN EXTENDED WILSON CURRENT-MIRROR SELF-BIAS BOOSTING CIRCUIT**



(57) Abstract: An amplifier circuit (1) includes an amplifying transistor (Q0) and a do bias circuit (2) for biasing the amplifier transistor (Q0) to obtain a conduction angle of at least about 180°. The do bias circuit (2) includes a self-bias boosting circuit which has a Wilson current-mirror circuit (Q2-Q6) having an output coupled to a control terminal of the amplifying transistor (Q0) by a resistor (R1), and a capacitor (C2) coupled from the extended Wilson current-mirror circuit (Q2-Q6) to a common terminal (Gnd).



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*For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.*